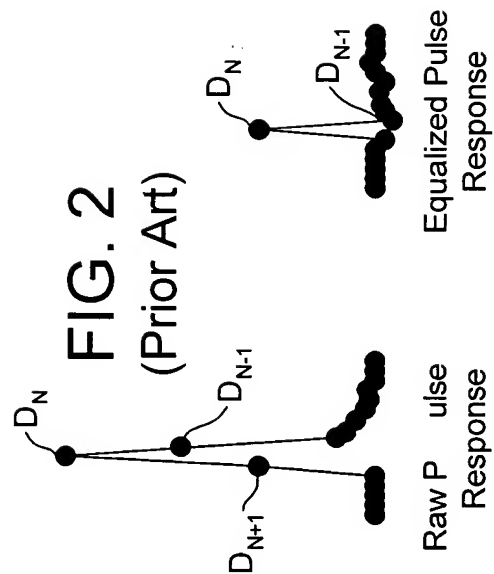
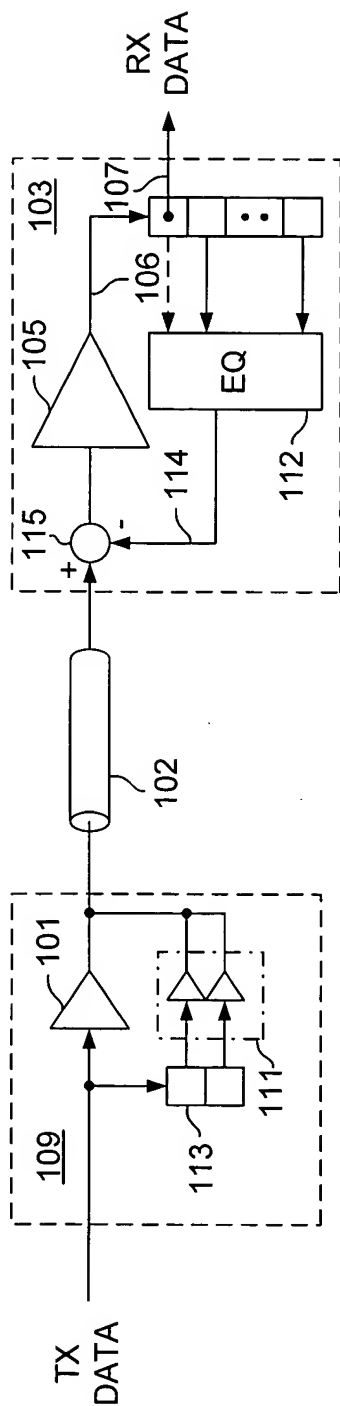


FIG. 1
(Prior Art)



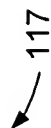


FIG. 4

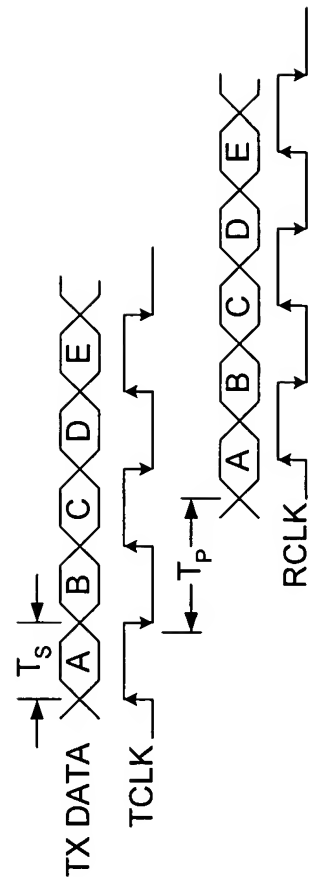
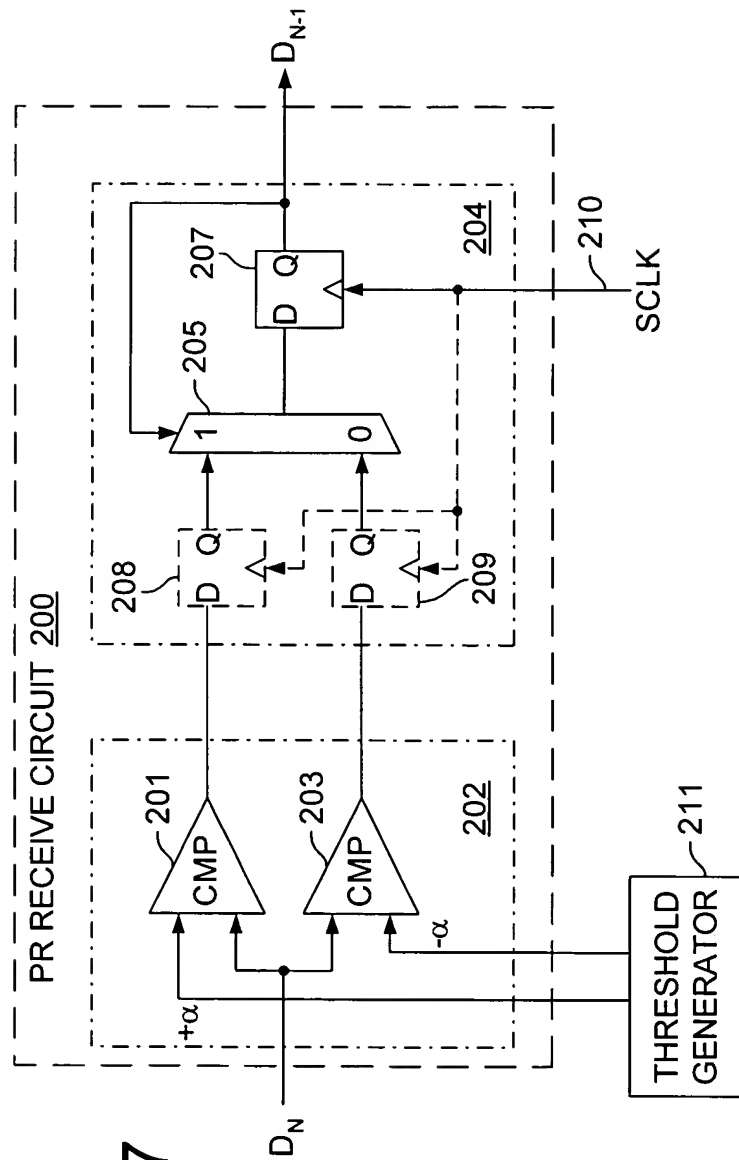


FIG. 7



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FIG. 9

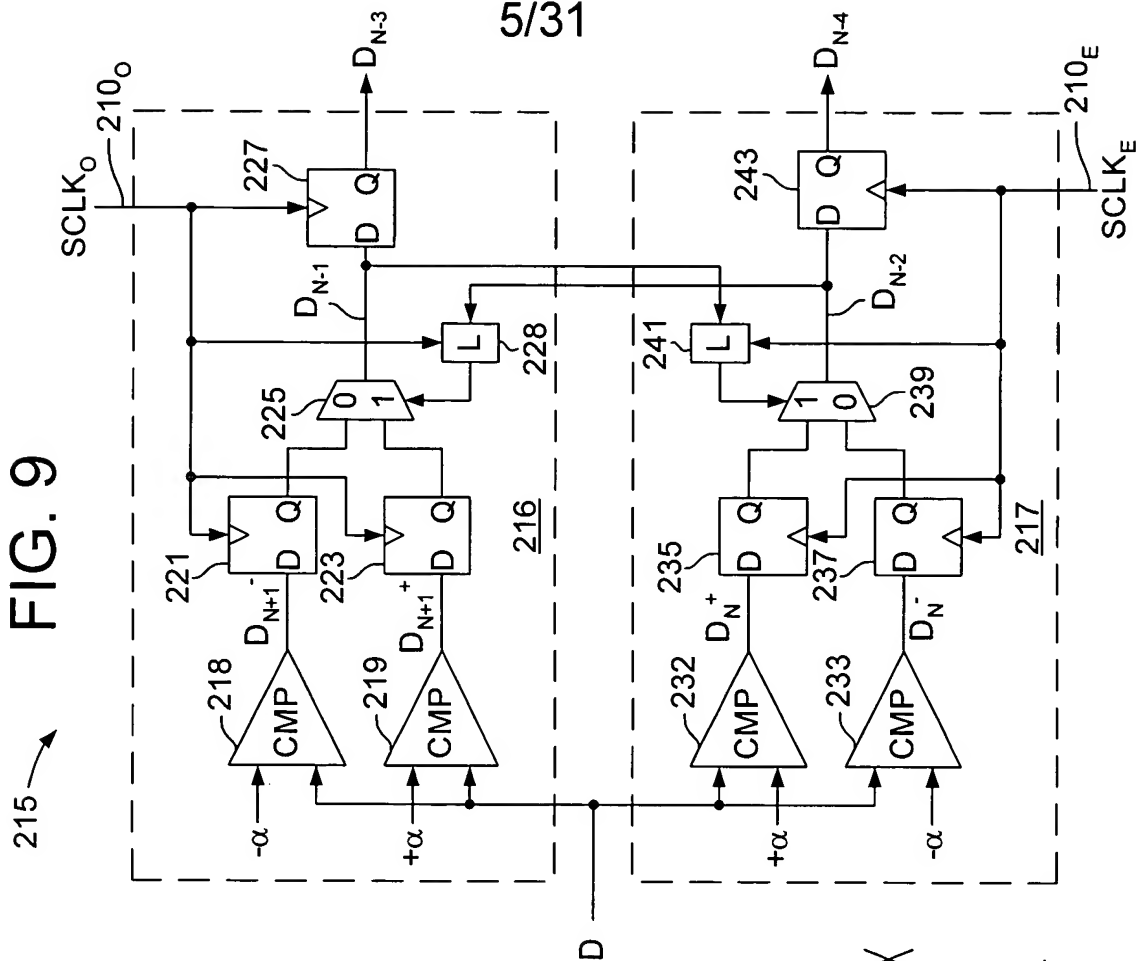


FIG. 8

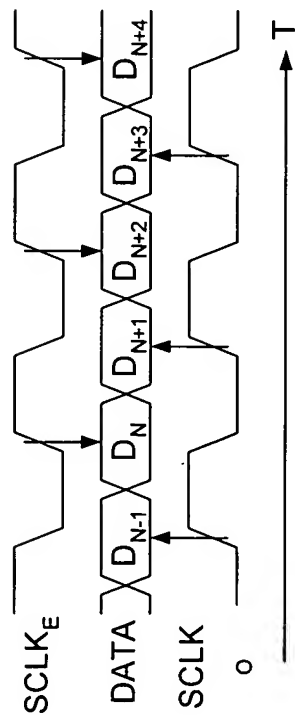
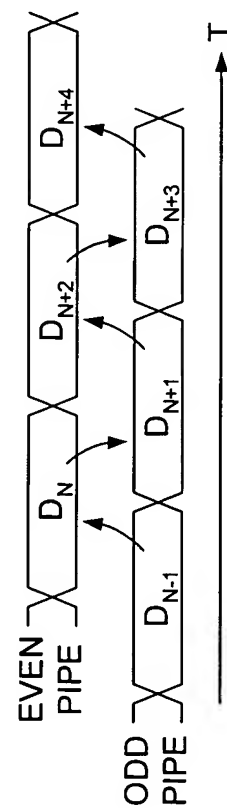


FIG. 10



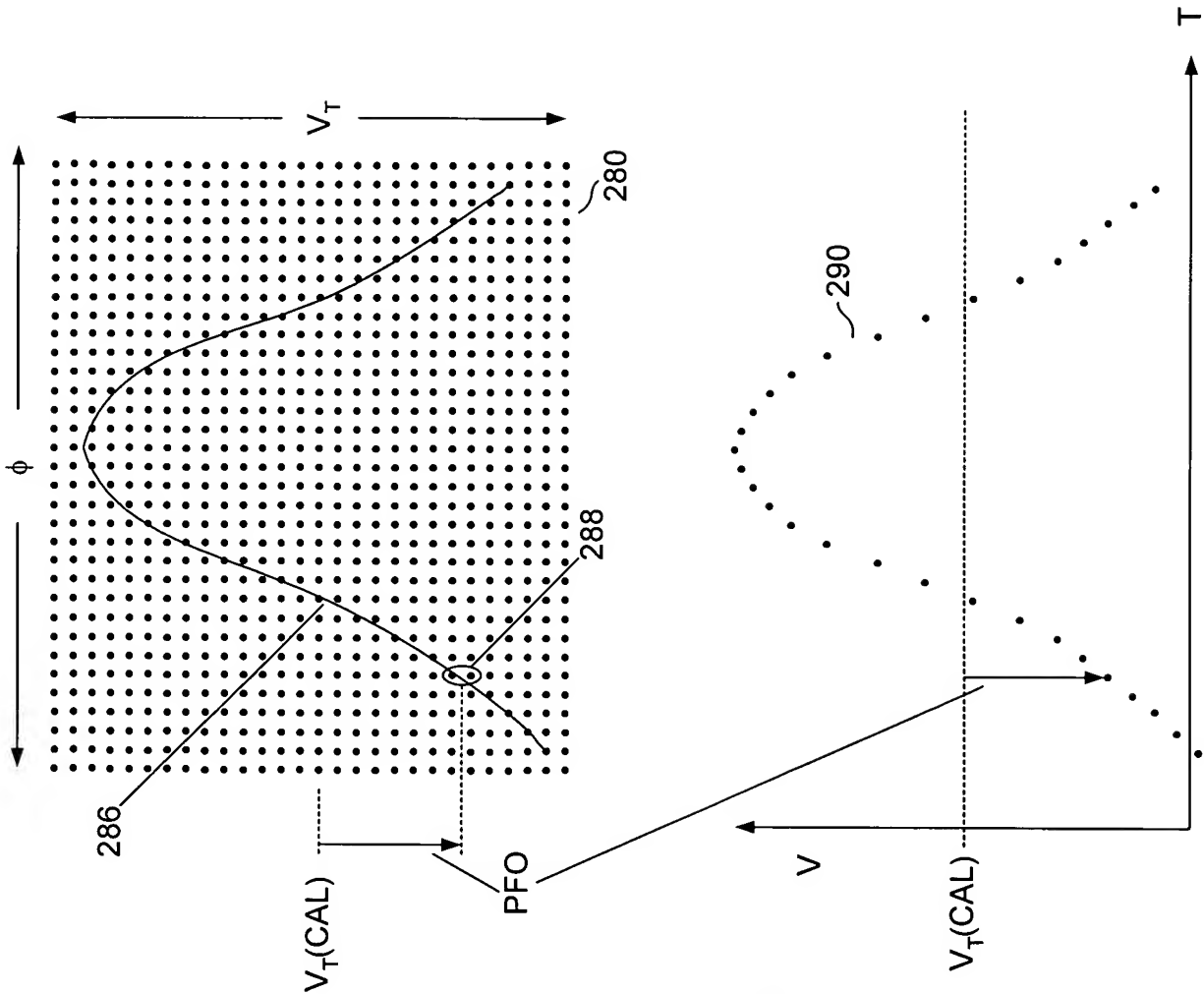


FIG. 11

FIG. 12

The diagram illustrates a system 300 for generating a signal 122. The system is divided into two main sections by dashed lines.

Left Section (System 300):

- PATTERN GENERATOR 303:** Outputs a signal 305 (00..00100..00) to a multiplexer 307.
- APPLICATION LOGIC 302:** Outputs TX DATA 306 to the multiplexer 307.
- Multiplexer 307:** Selects between the PATTERN GENERATOR 303 and the APPLICATION LOGIC 302 based on a control signal 304 (input 0) and outputs a signal 309.
- SCOPE 301:** Receives the signal 309 from the multiplexer 307.
- Buffer 122:** Receives the signal 309 from the multiplexer 307 and outputs a signal 122.

Right Section (System 309):

- Buffer 310:** Receives the signal 309 from the multiplexer 307 and outputs a signal 312 to the APPLICATION LOGIC 315.
- APPLICATION LOGIC 315:** Receives the signal 312 from the buffer 310 and outputs a signal to the AND gate 313.
- AND gate 313:** Receives the signal from the APPLICATION LOGIC 315 and the signal 312 from the buffer 310. Its output is input to the PREG block 311.
- PREG block 311:** Receives the signal from the AND gate 313 and outputs a signal to the buffer 310.

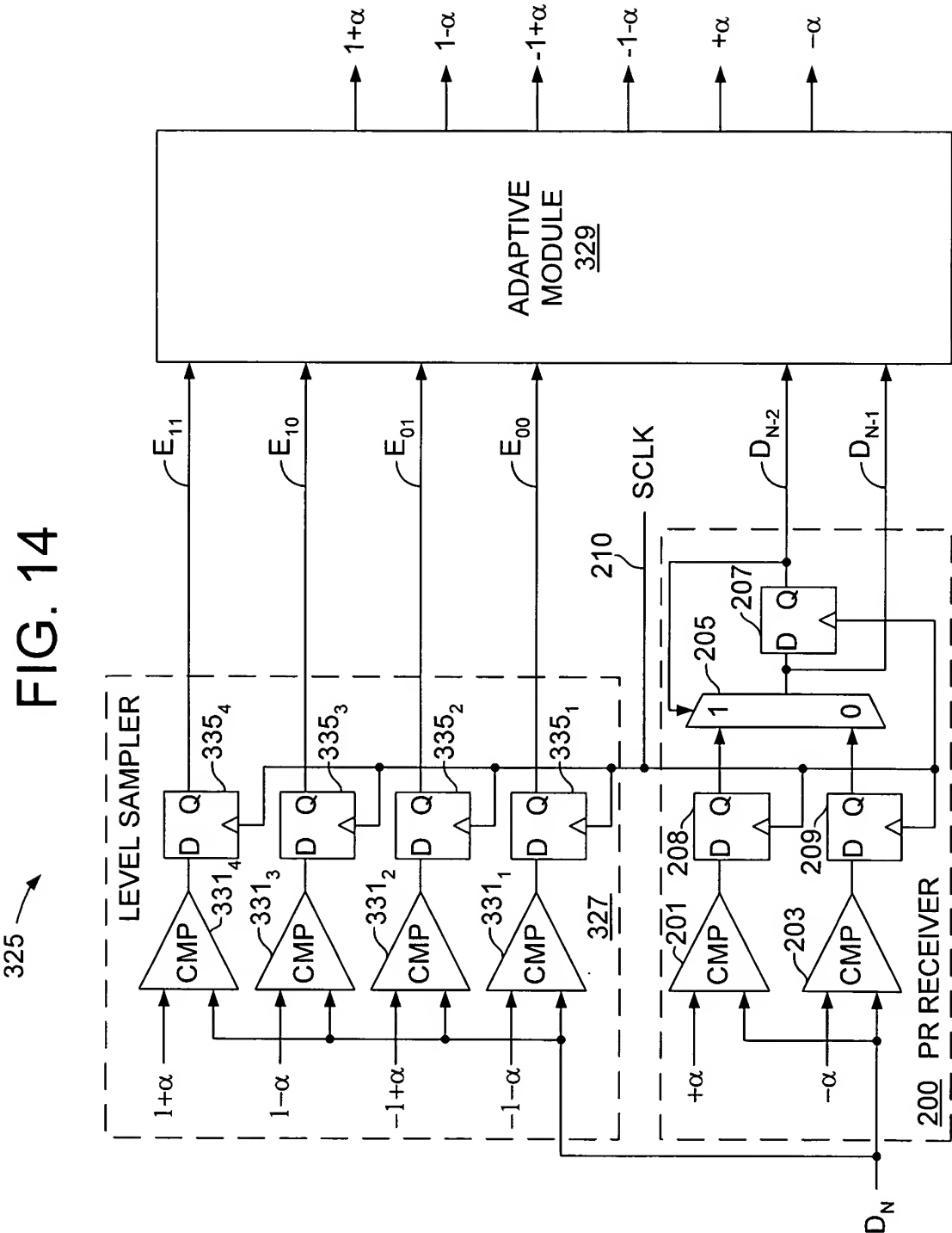


FIG. 15

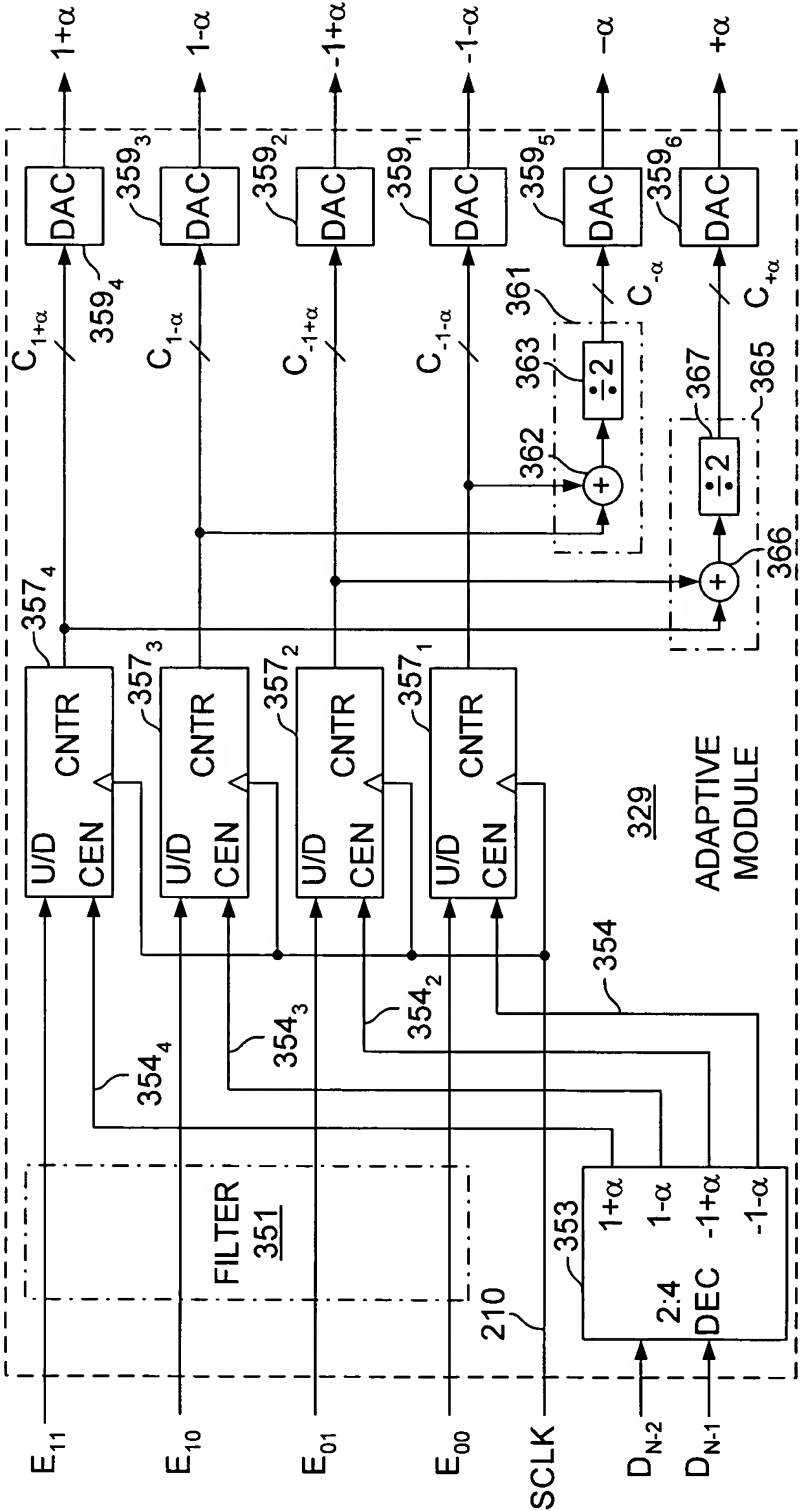


FIG. 16

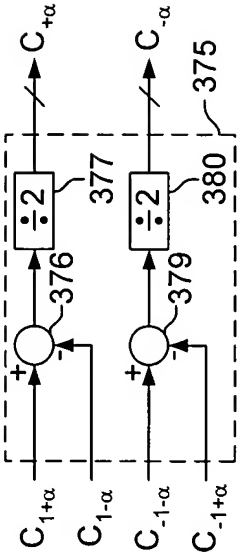
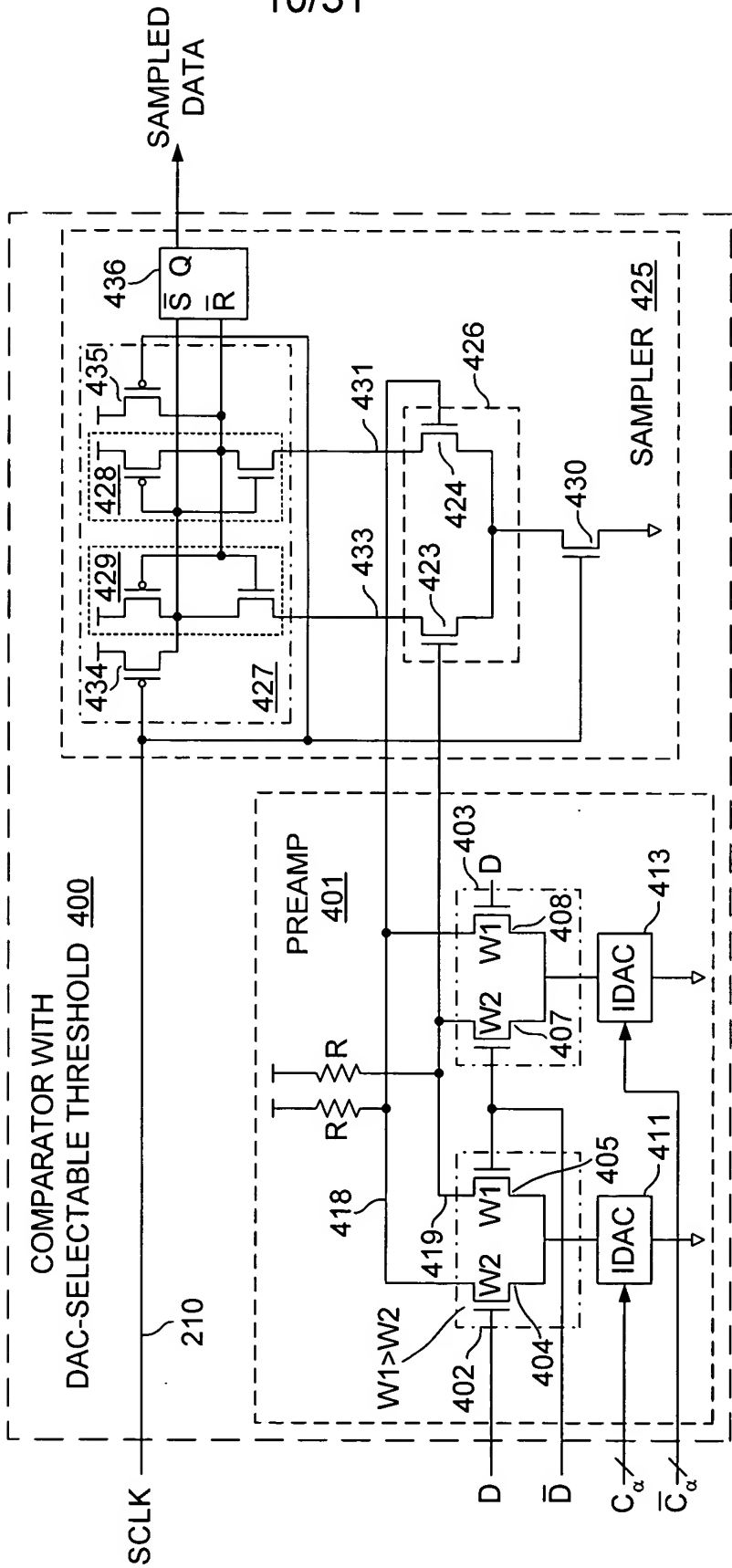


FIG. 17



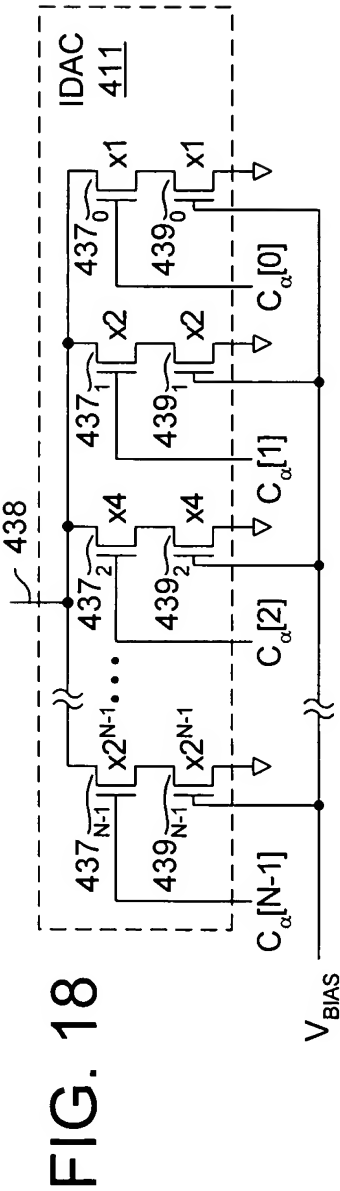
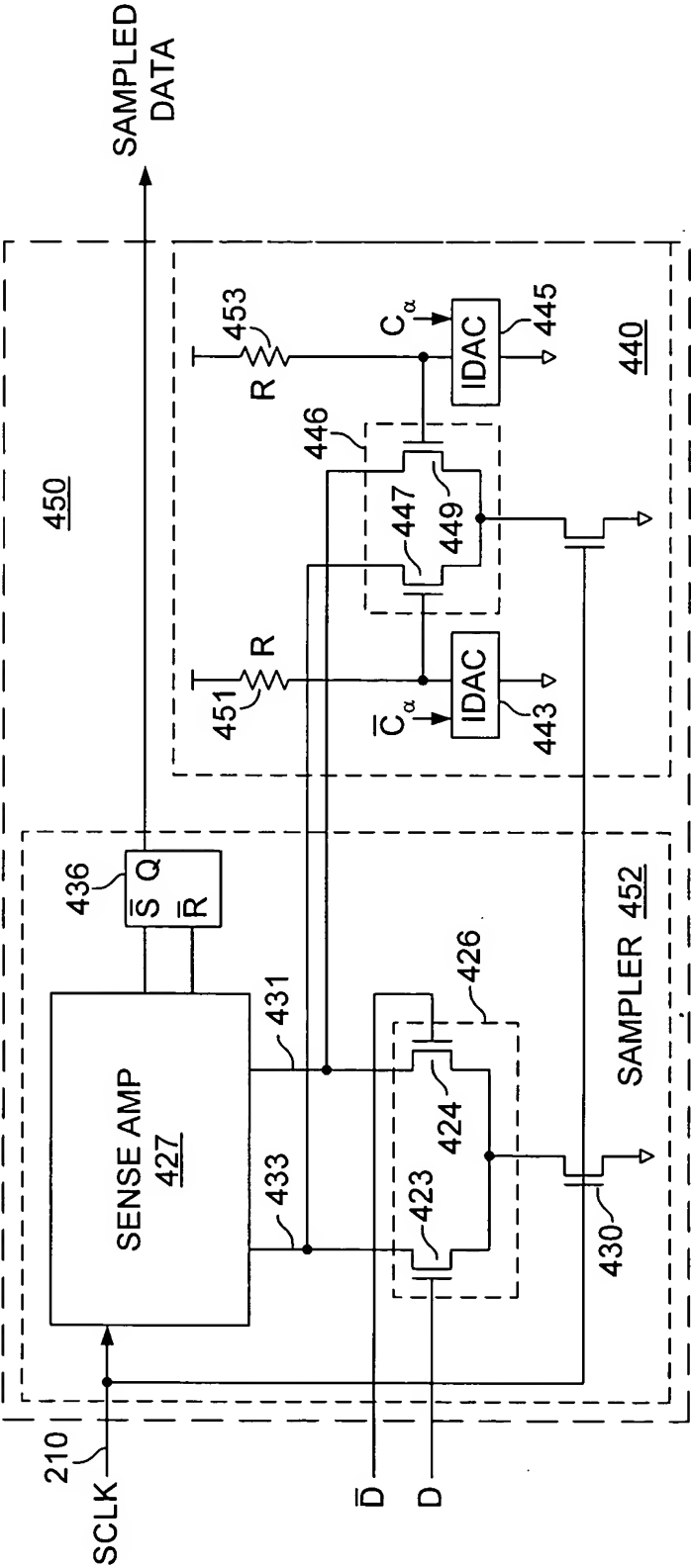
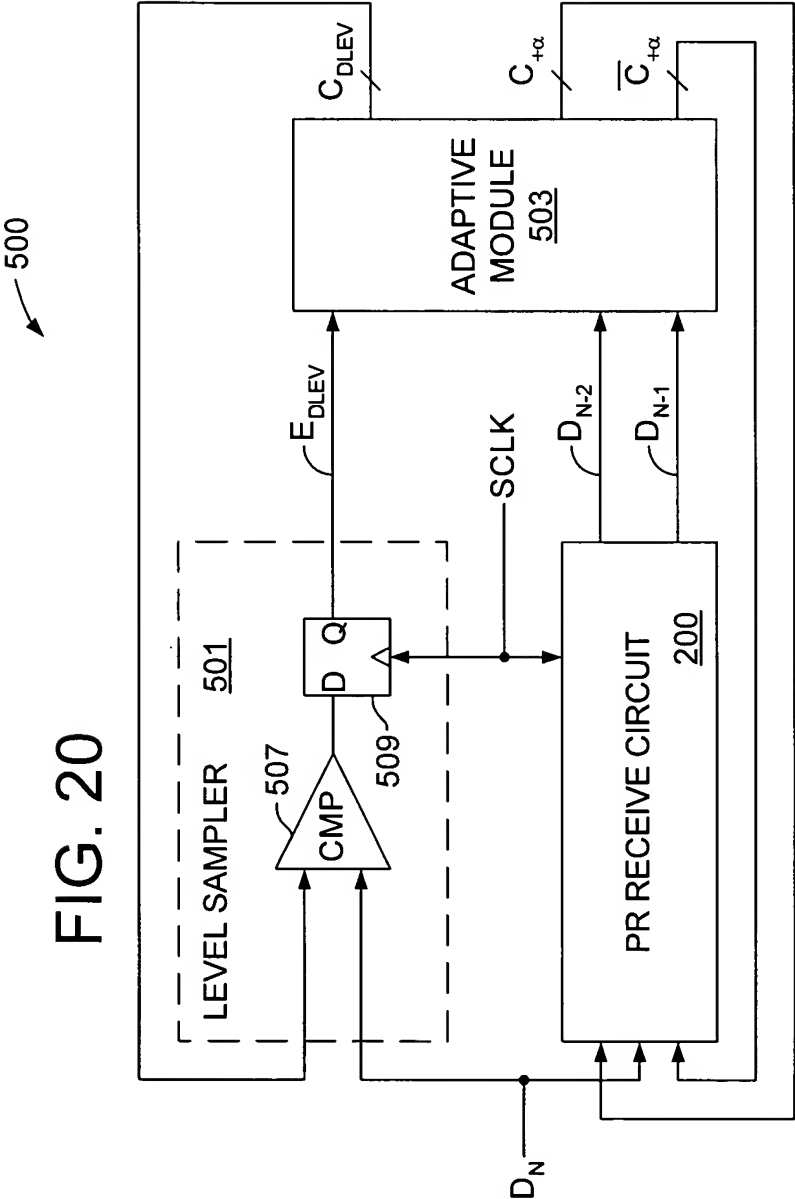


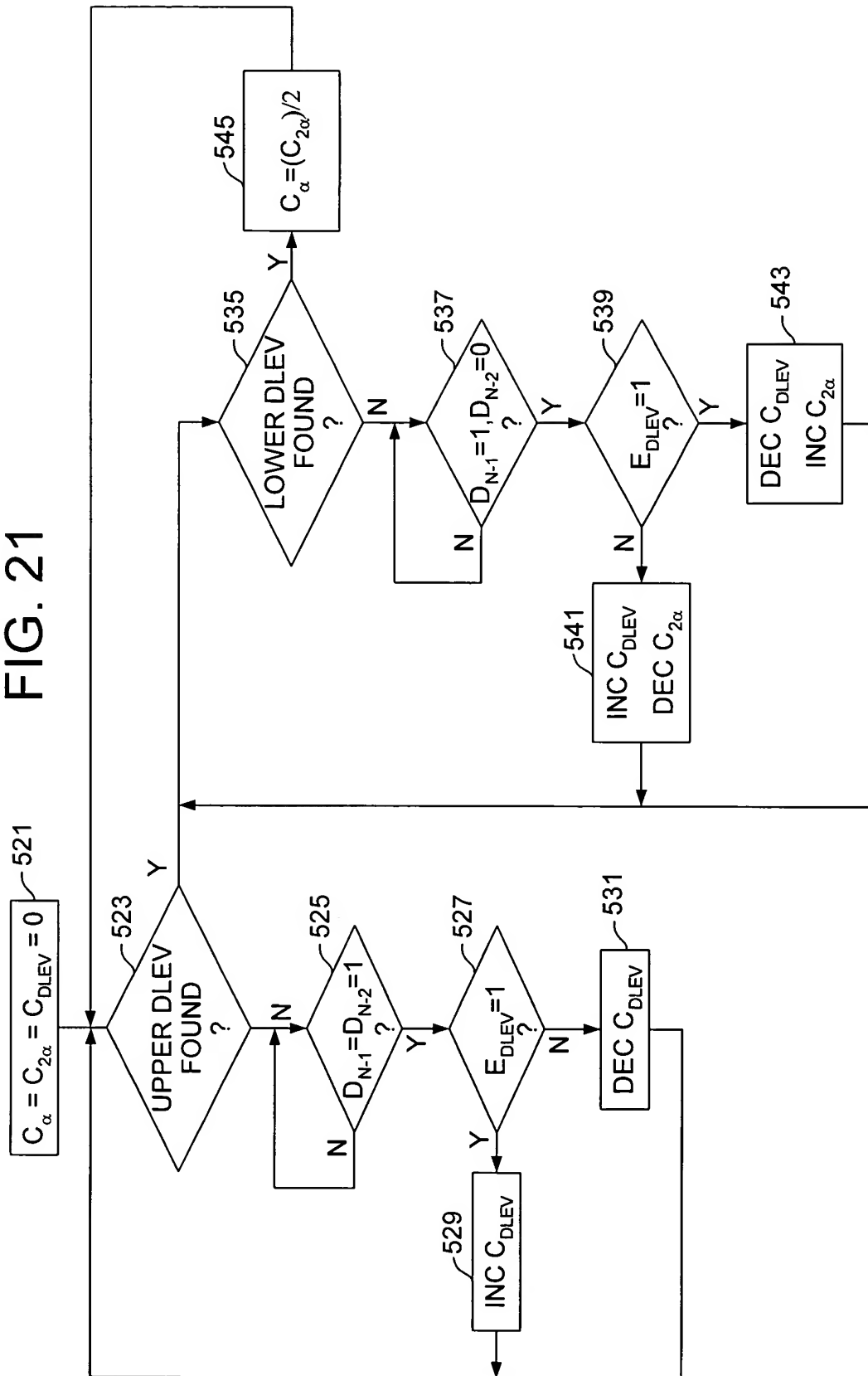
FIG. 19





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FIG. 21



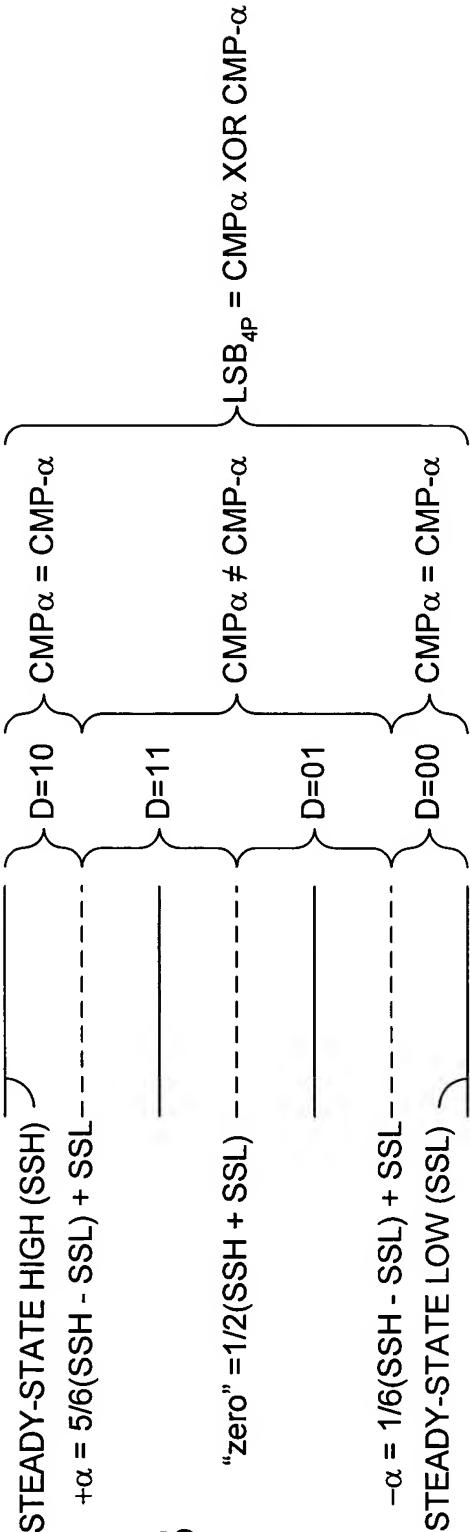
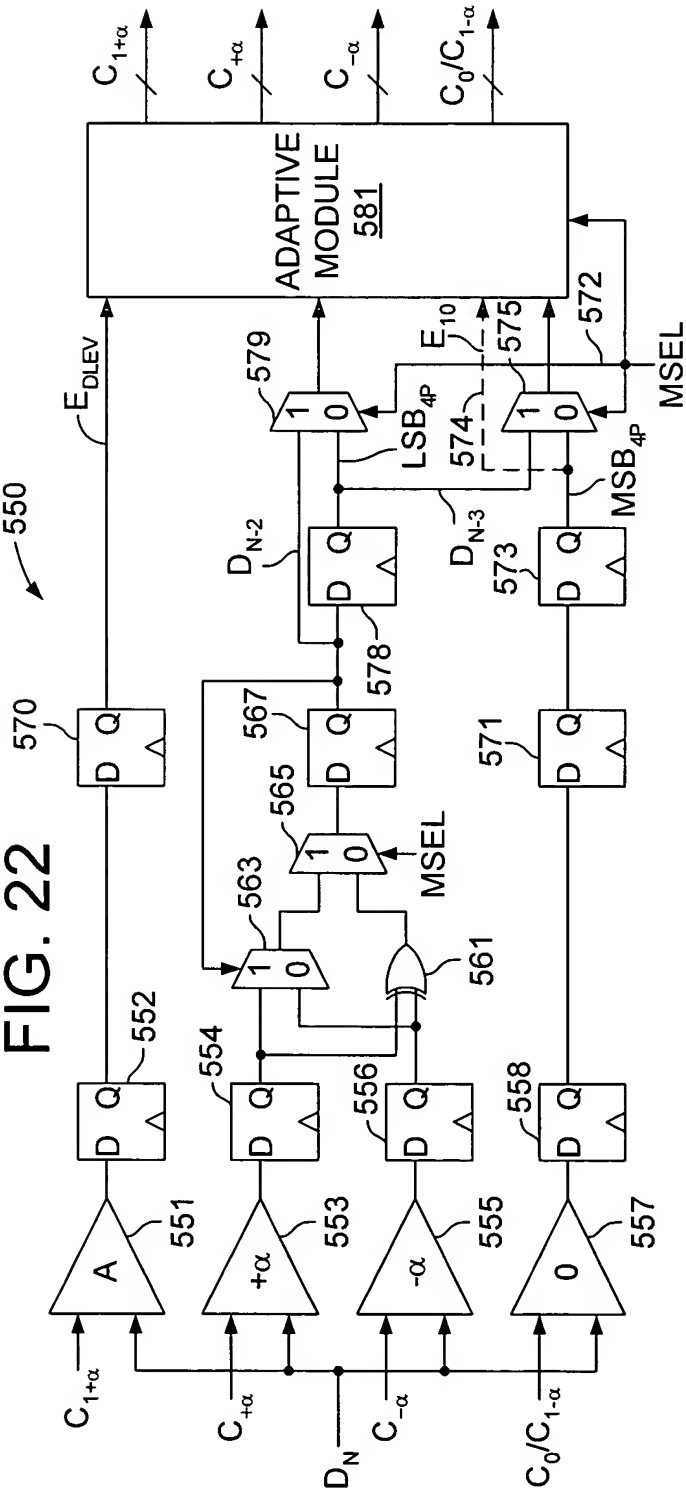


FIG. 27

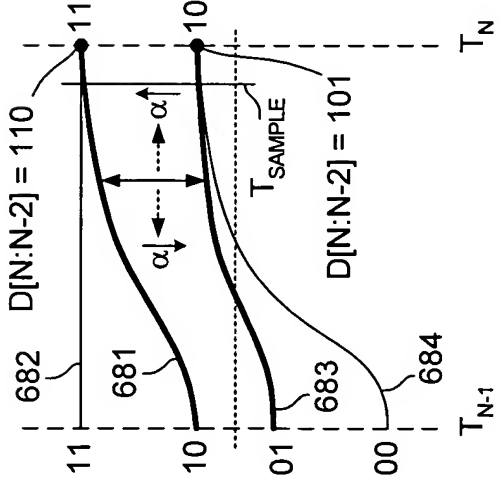
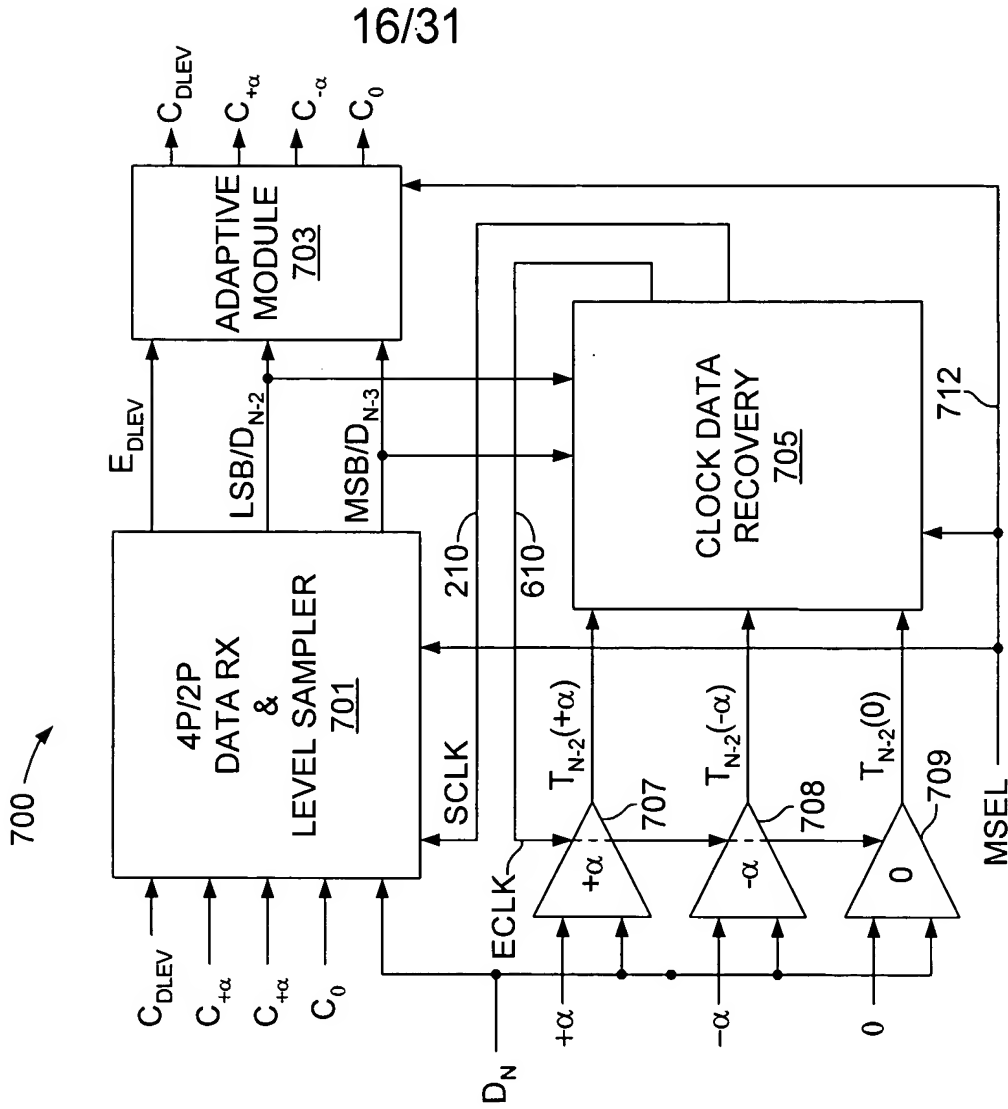


FIG. 28



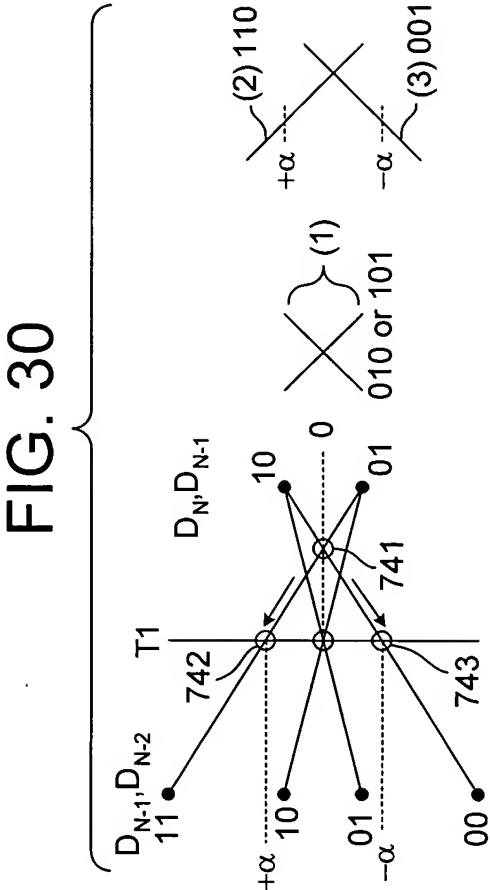
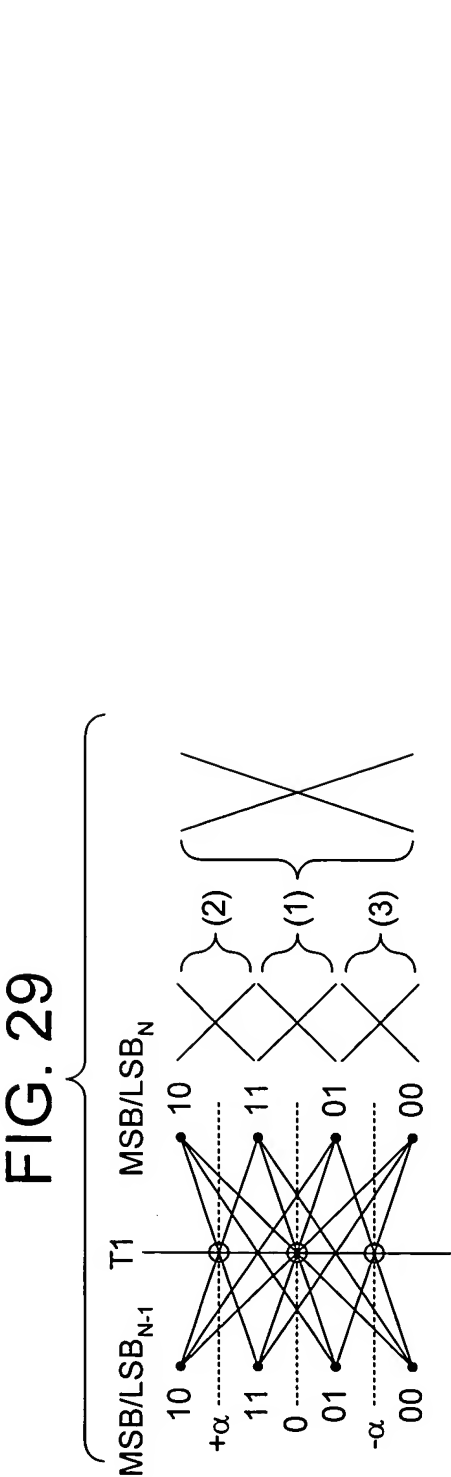


FIG. 31

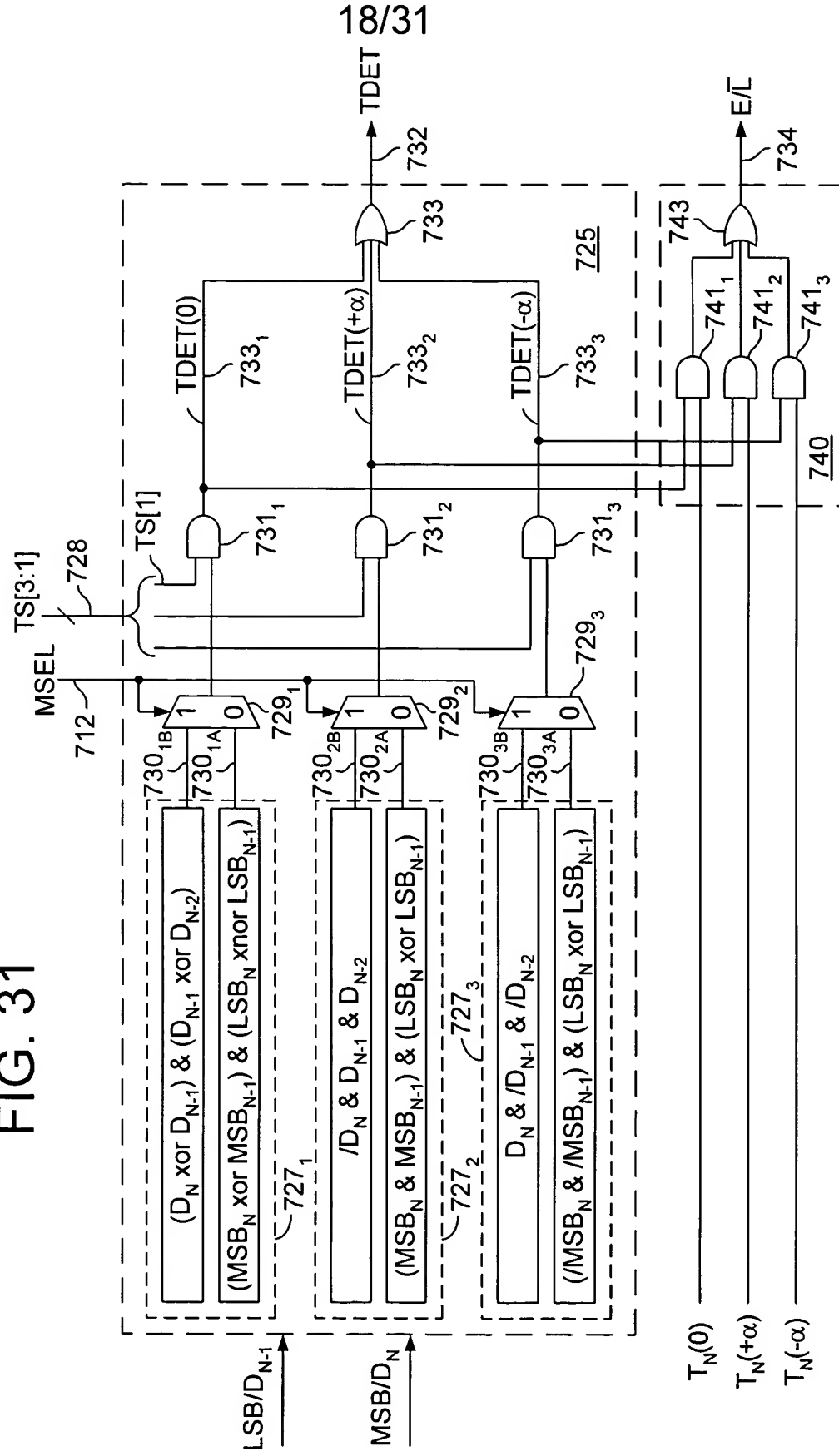


FIG. 32

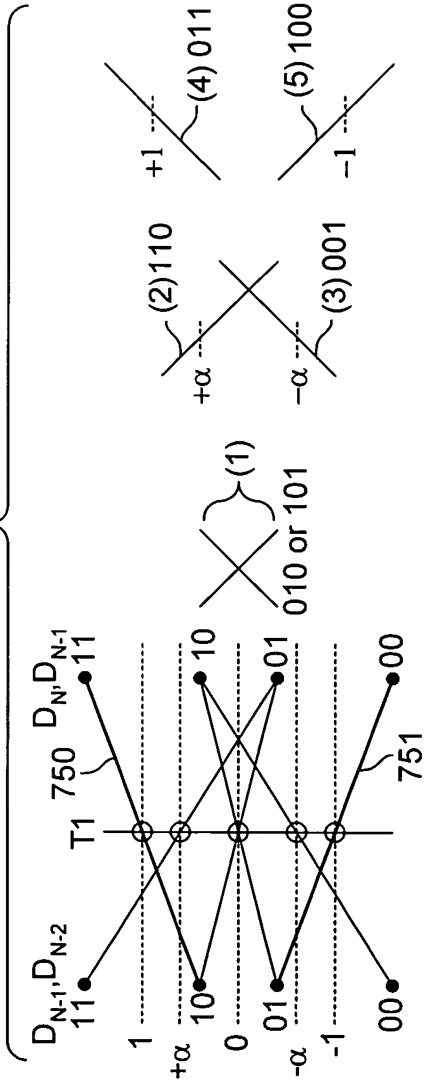
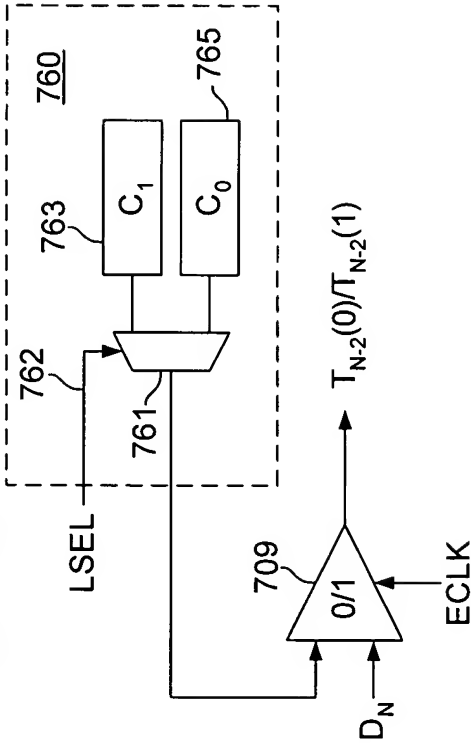


FIG. 33



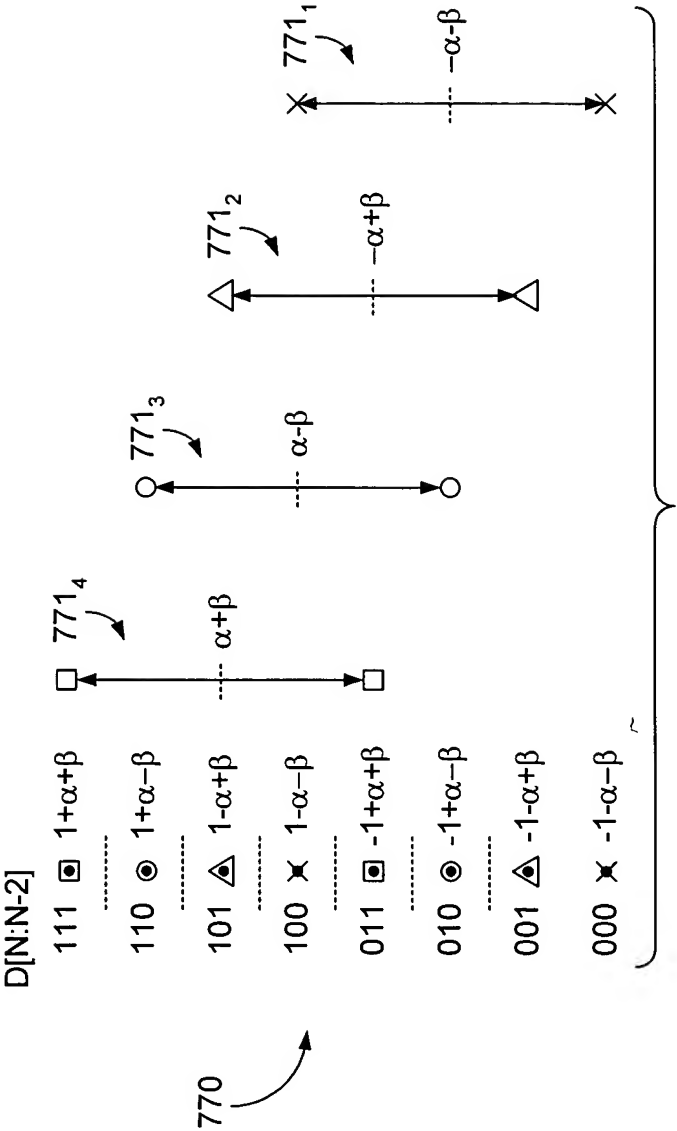
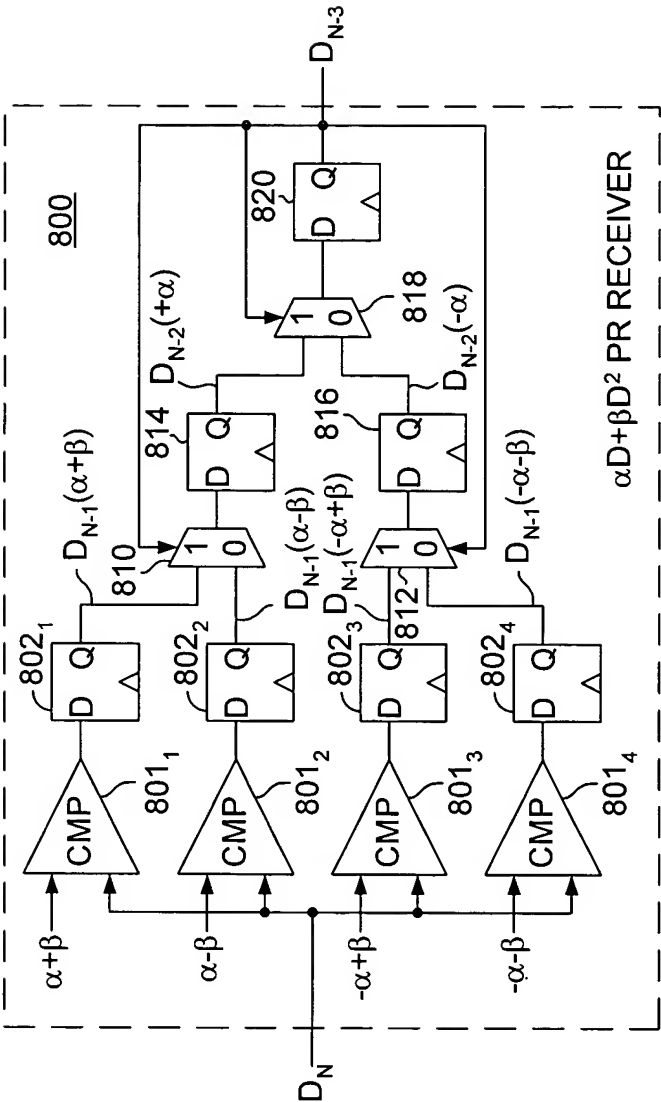


FIG. 34

FIG. 35



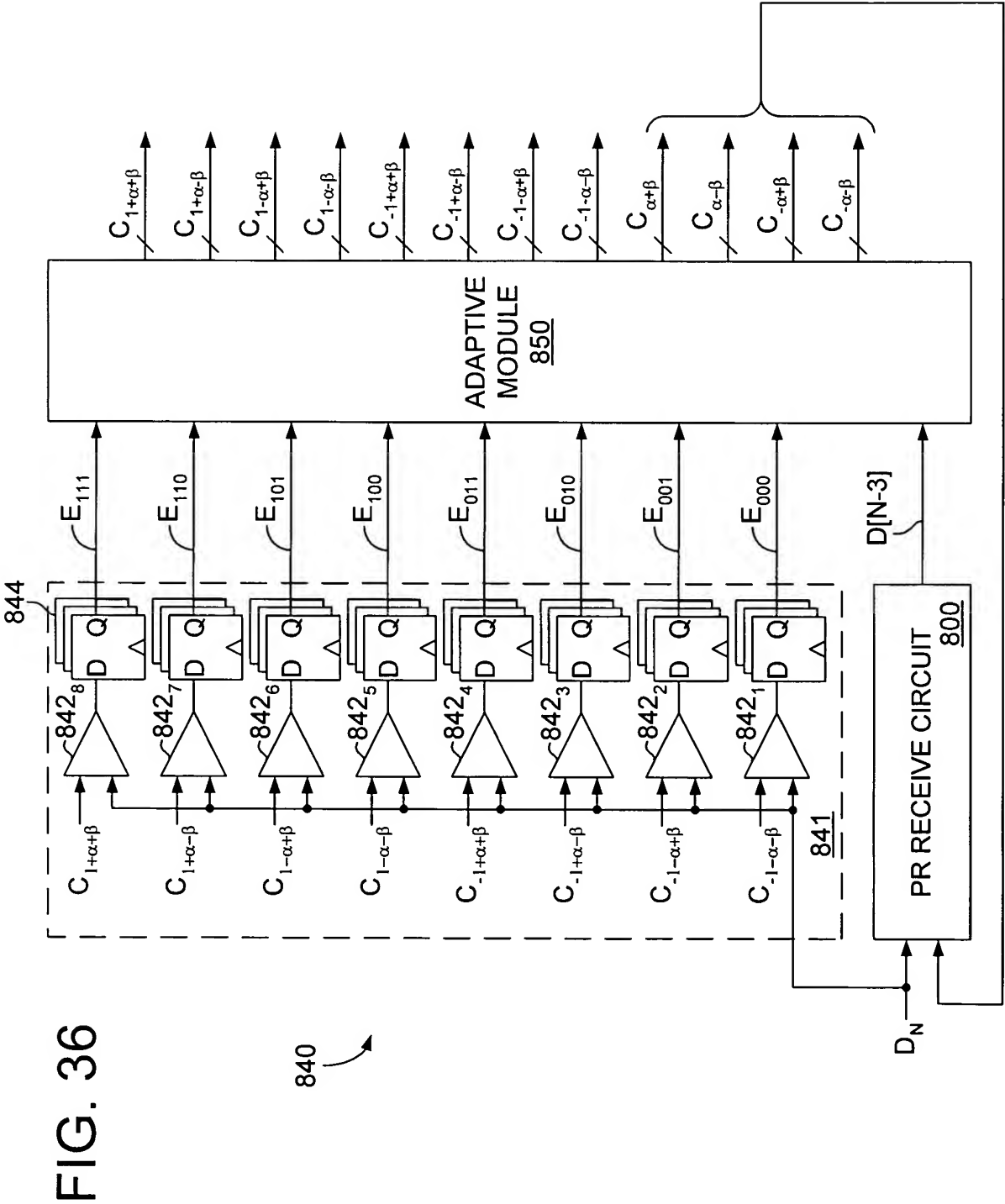


FIG. 37

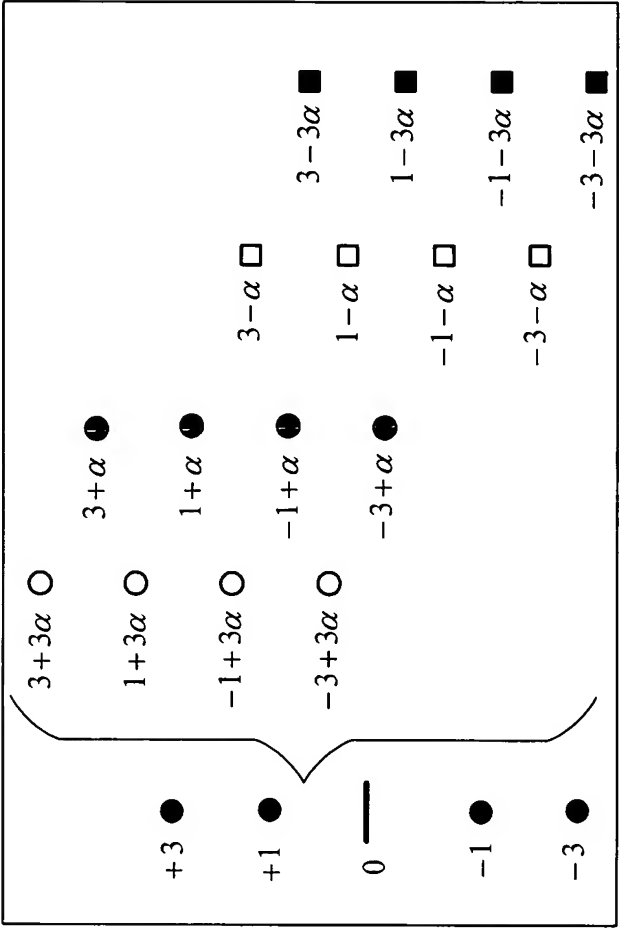
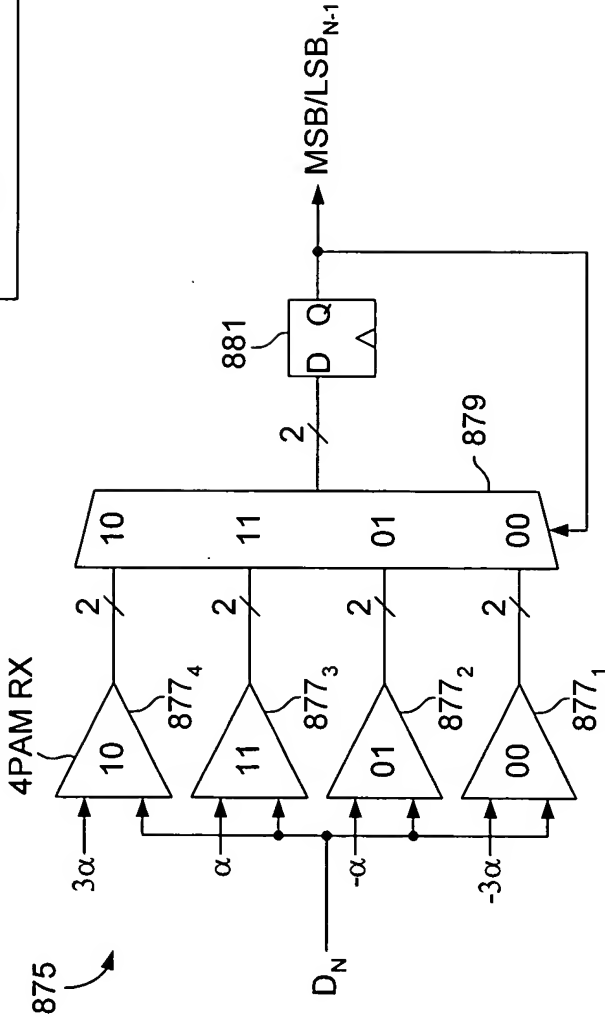


FIG. 38



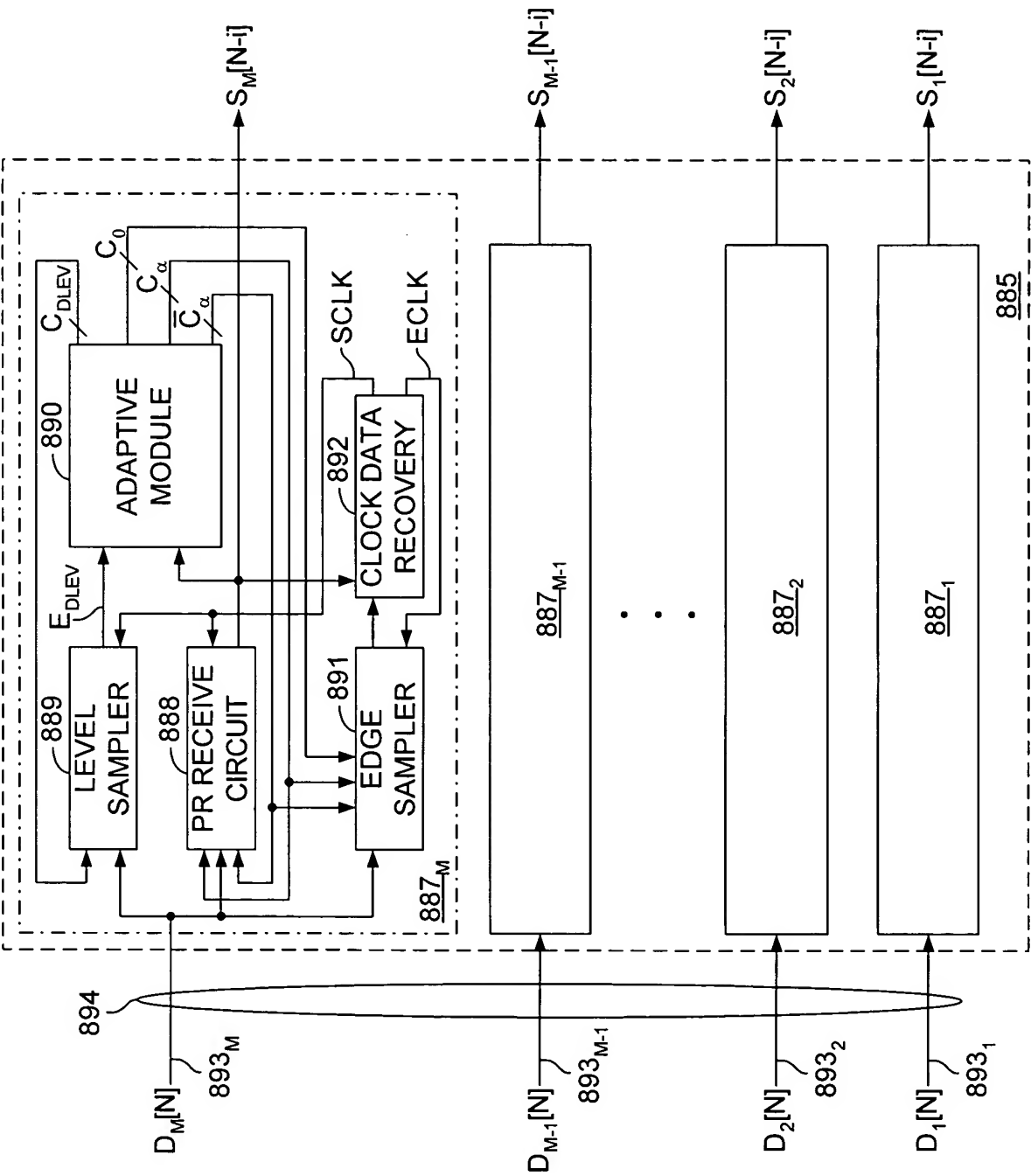
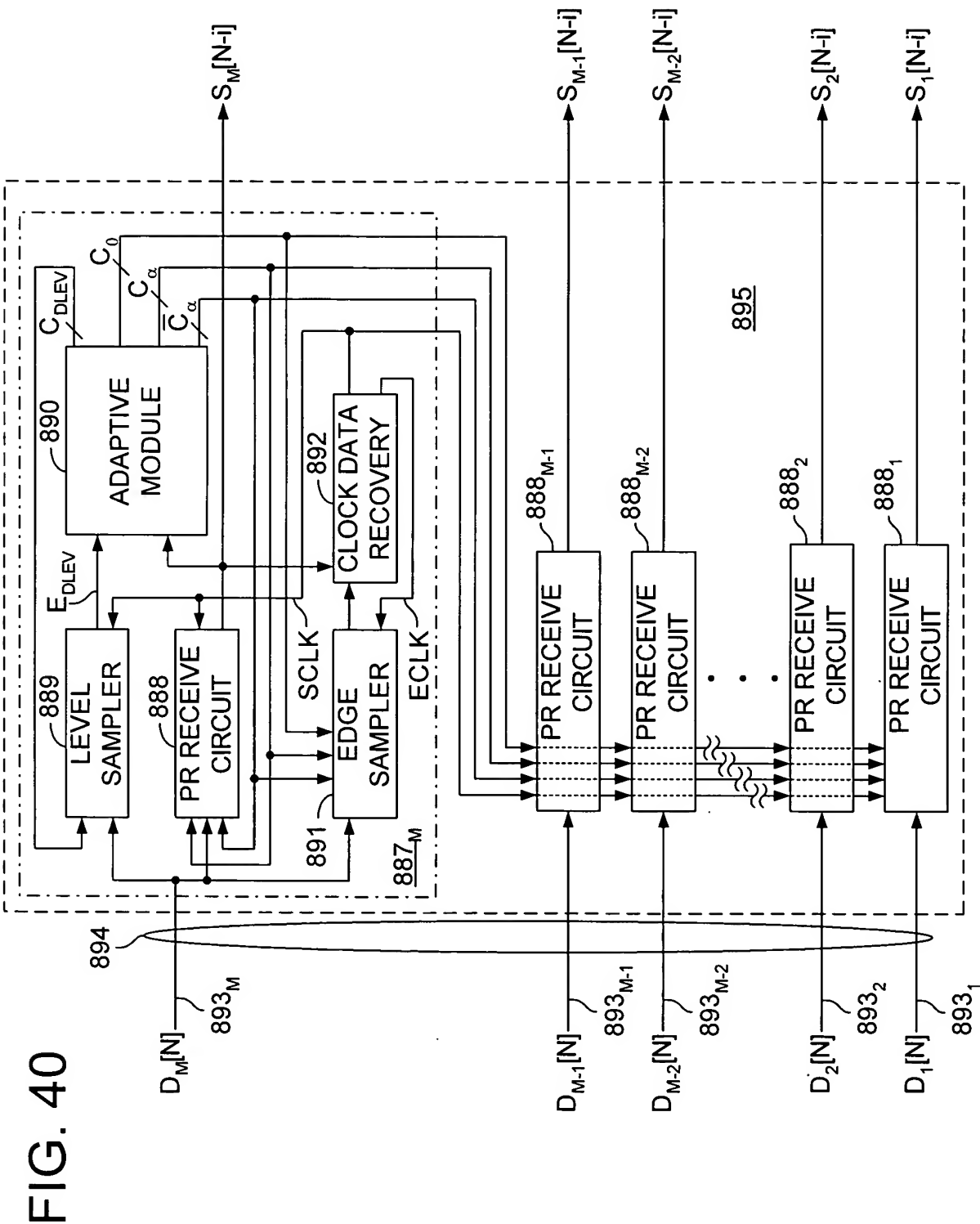
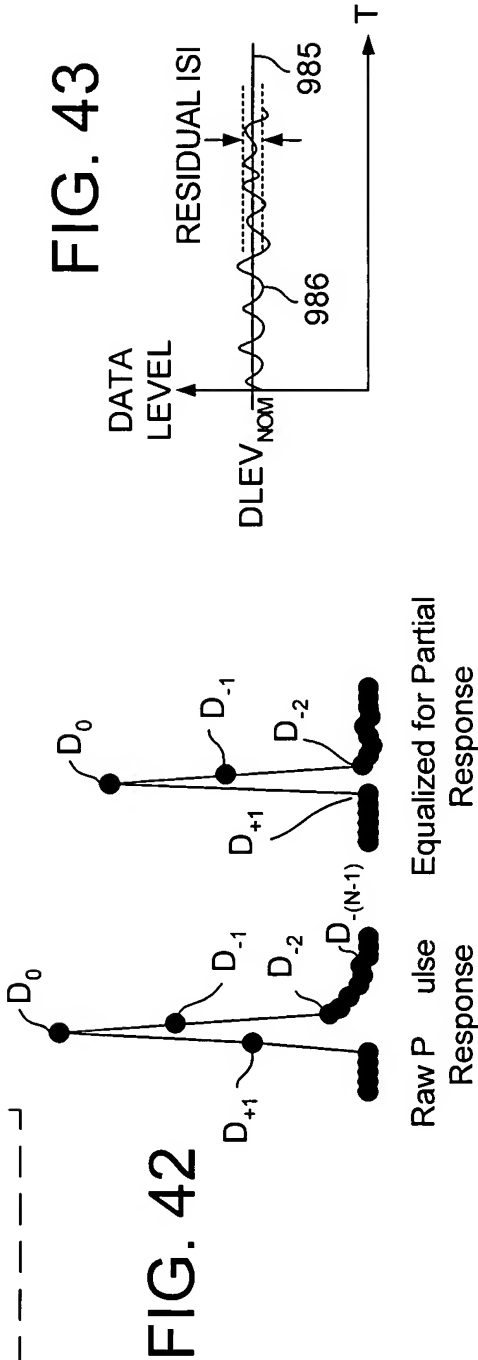
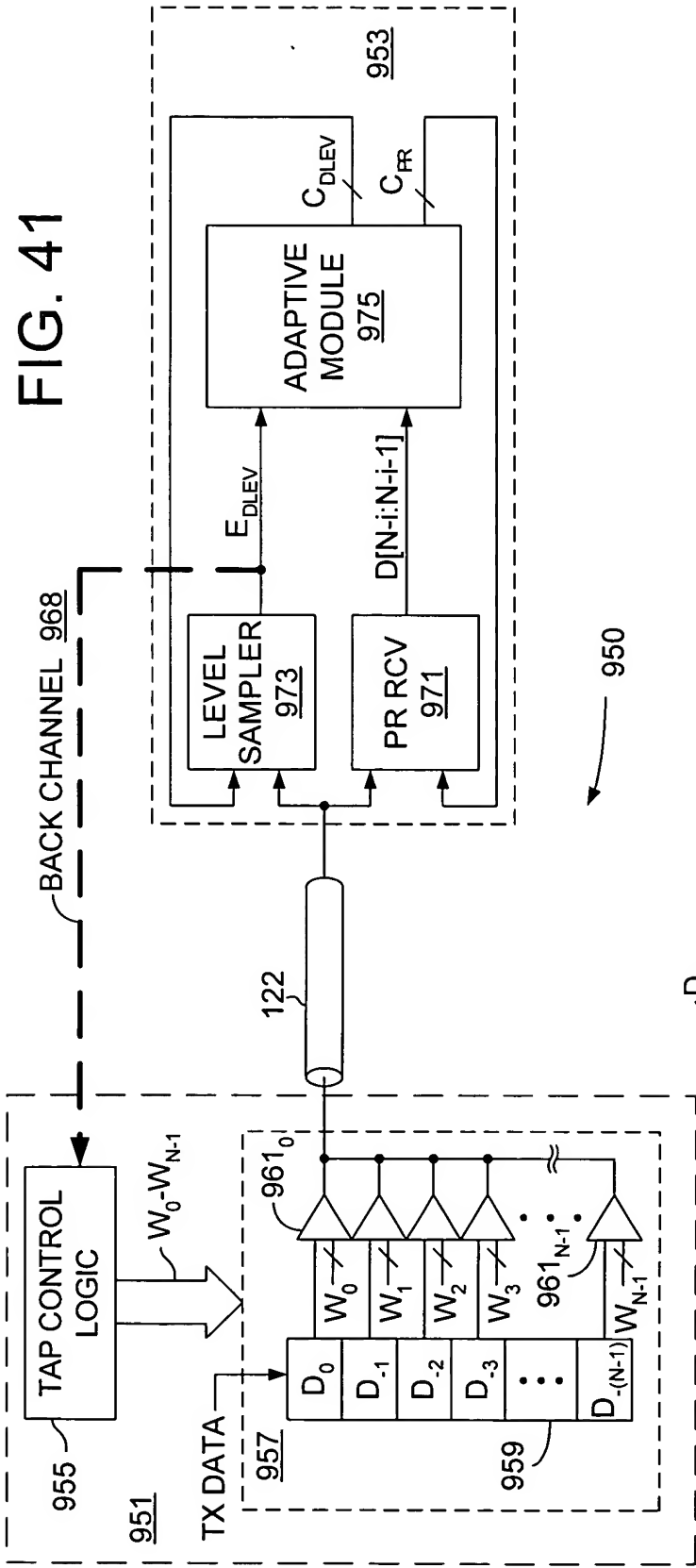


FIG. 39





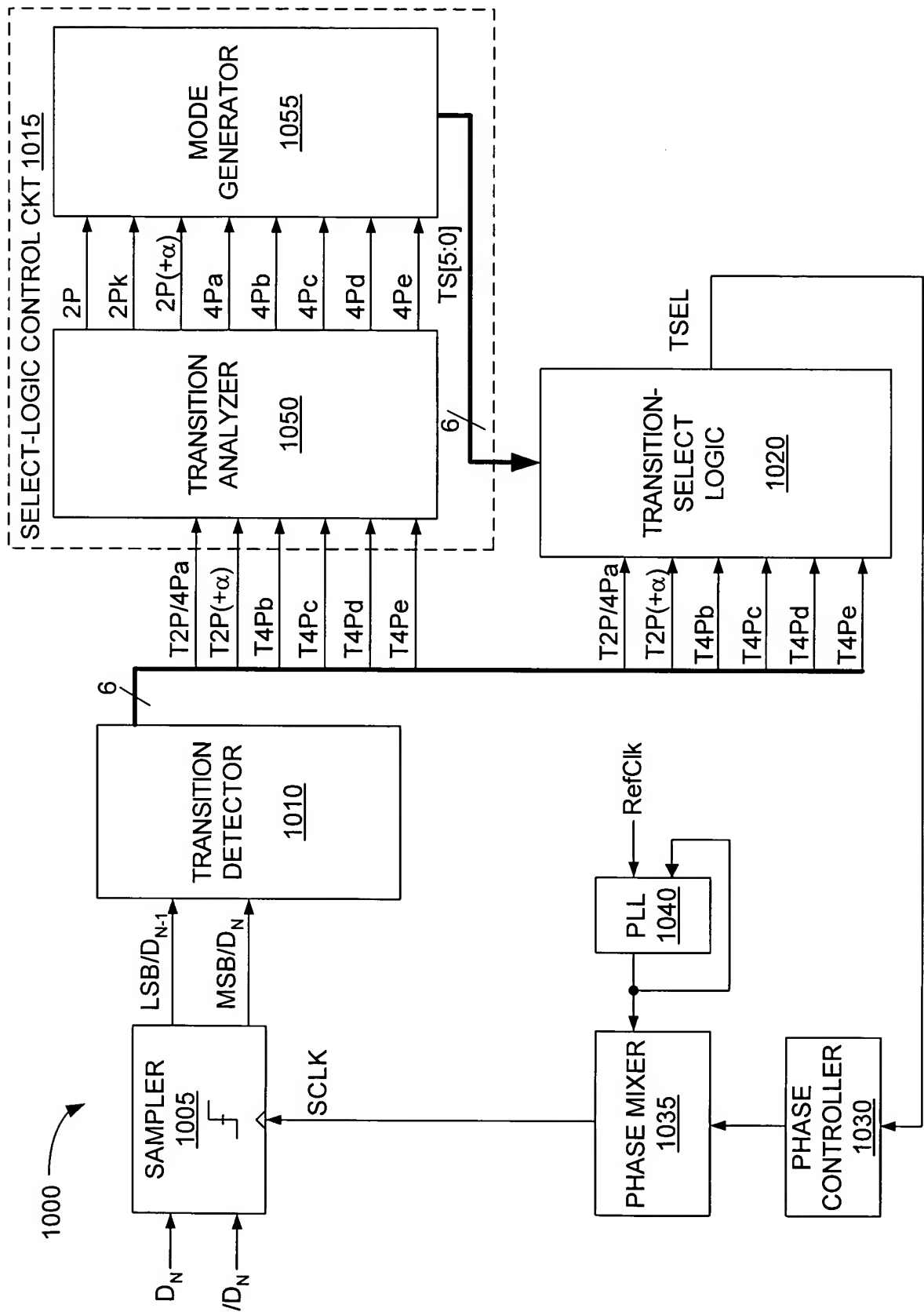


FIG. 44

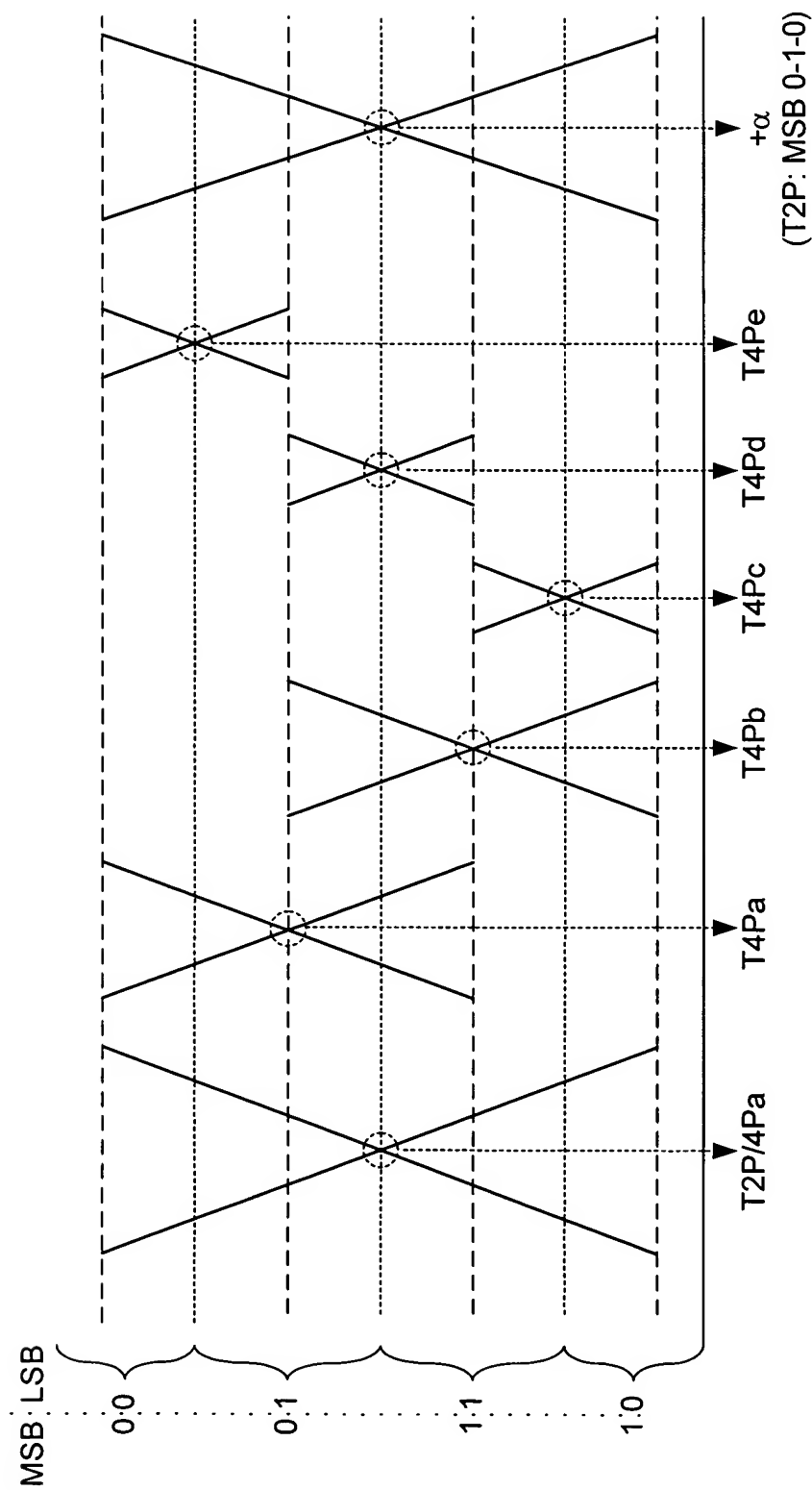


FIG. 45

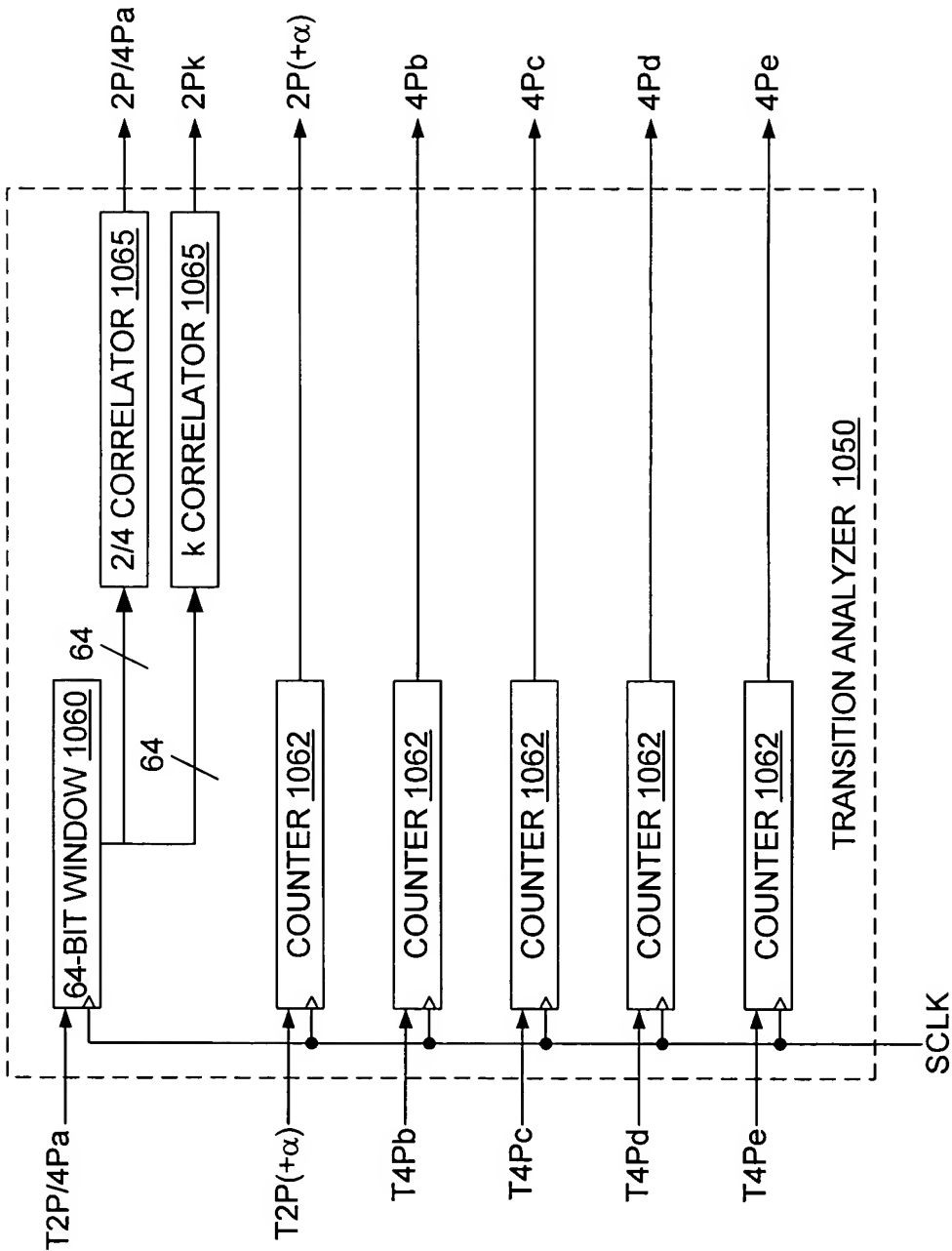


FIG. 46

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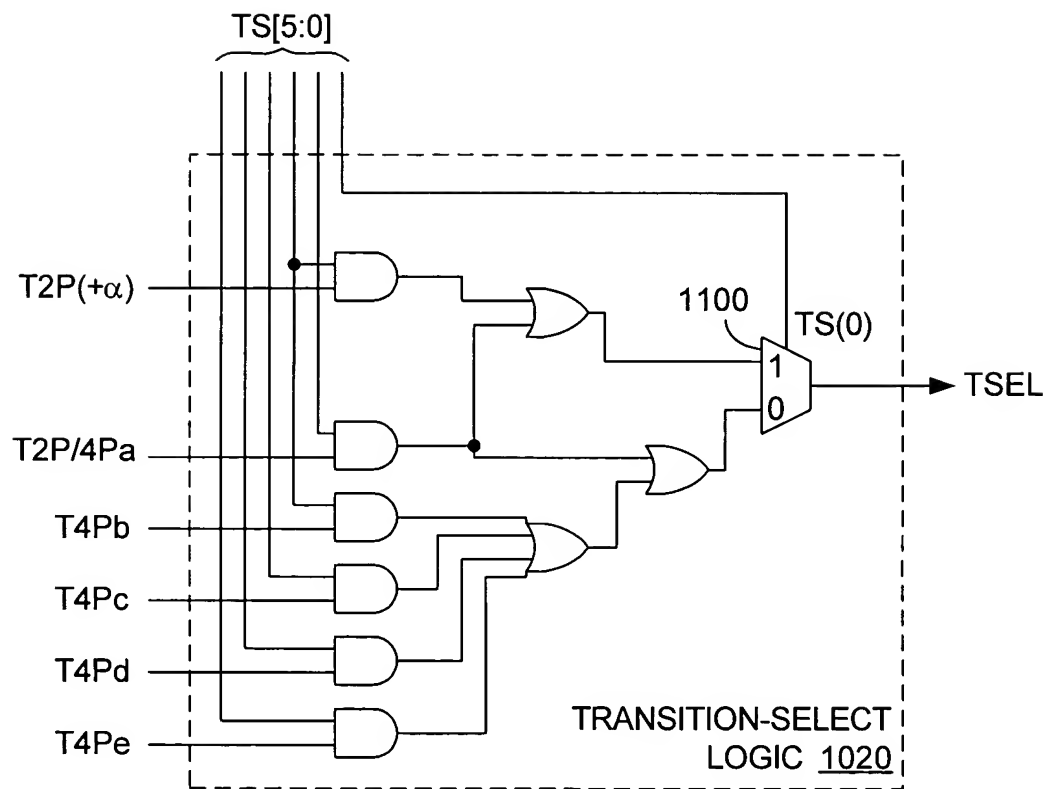


FIG. 47

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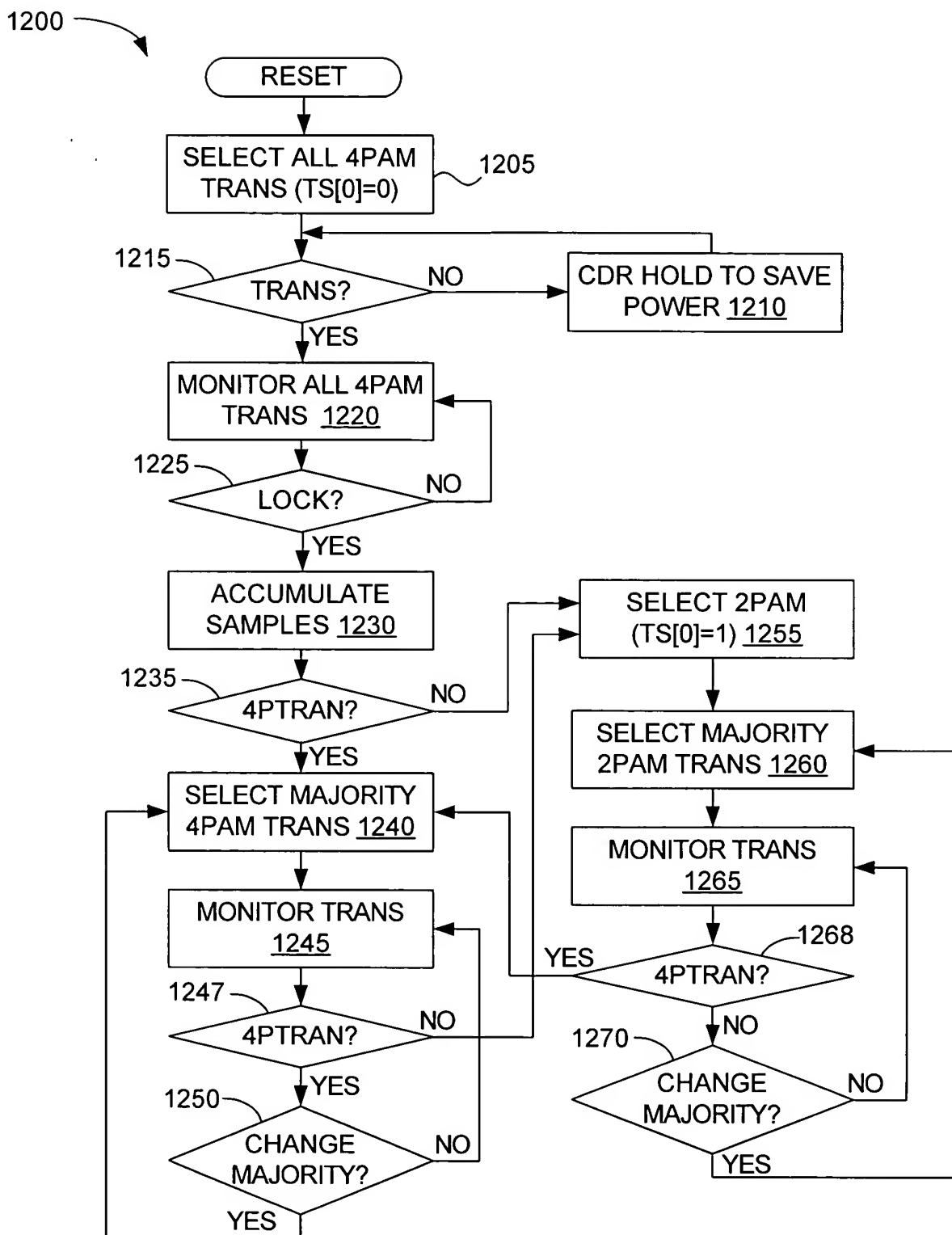


FIG. 48